

# VHDL / 1076 Status Update

- WG active with 2 PARs open - details in previous DASC meeting report
- Amendment Par: P1076C = VHDL + VHPI.
  - P1076C has been submitted to RevCom for their early consideration schedule and is currently being reviewed.
  - I have responded to feedback as needed.
- Revision Par: P1076 = P1076C + VHDL-200X + Accellera revisions
  - Par expires 31-Dec-2008
  - Expect Accellera to forward their VHDL-2006 3.0 standard for IEEE VASG consideration and future ballot sometime in June/July 2007.
- VASG website: <http://www.eda.org/vasg>
  - Chair: Jim Lewis [jim@synthworks.com](mailto:jim@synthworks.com)
- Officers last elected on Dec 16, 2005, no elections due.
- P&Ps approved on: 4-Nov-2005 posted on VASG website
- Voting records and voting membership posted on VASG website

# VHDL / 1076 Relationships

- Dual Logo standards numbering under IEEE
  - Reported to IEEE-SA the problems our users are having due to IEEE renumbering the standards using IEC numbering.
  - Still waiting for response
- Accellera
  - Expecting Accellera VHDL-2006 Draft 3.0 or updated revision to be forwarded in June or July 2007.
- IEC
  - Forwarded draft of P1076C amendment after ballot updates to Alex Z.
  - I would like to encourage interested parties of IEC TC 93 to become active in the Accellera process.
    - IEEE-IEC proposed process suggests editing LRM drafts to exchange ideas - this is a time and cost prohibitive effort
    - By participating in the Accellera VHDL TSC, we can review and exchange ideas at the requirements and detailed enhancement levels - which I think will bring us to consensus quicker.